

## ABSTRACT OF THE DISCLOSURE

In a data transfer device, a selector selects one of delayed clocks 0 to 3, according to random numbers generated by a random number generating circuit.

- 5     Output from an output terminal of the selector is an output clock whose timing of transition to a HIGH level or a LOW level is randomly changed. Timing to switch output data to be output from each of flip-flops is also randomly changed. As a result, an energy density at some frequency of electromagnetic wave radiated from a signal lines is not raised, so that radiant noises and crosstalk between signals may be
- 10    reduced.